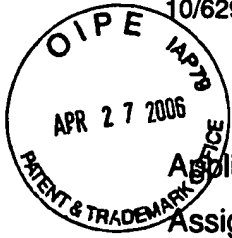


X-1173 US  
10/629,365

PATENT  
Conf. No.: 6255

IN THE UNITED STATES PATENT OFFICE



Applicants: Asim A. Bajwa et al.

Assignee: Xilinx, Inc.

Title: "Column Redundancy Scheme for Non-Volatile Flash Memory Using JTAG Input Protocol"

Serial No.: 10/629,365

File Date: July 29, 2003

Examiner: Tuan T. Nguyen

Art Unit: 2824

Docket No.: X-1173 US

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AMENDMENT AFTER NOTICE OF ALLOWANCE

FILED UNDER 37 CFR 1.312

Dear Sir:

Applicants have amended paragraph [0011], as indicated in the attached amendment to the specification. In particular, Applicants request the following amendment at page 6, line 2: change "221" to - 241 -. The amendment merely corrects a minor typographical error in paragraph [0011] and does not require substantive examination. In a telephone conversation with the Examiner on April 18, 2006, Applicants' attorney proposed the amendment and the Examiner agreed to enter the amendment if submitted by Applicants. Applicants thank the Examiner for his time and consideration.